



						2	v	
3v					x1x			
4v	x		7	x8	3			6
	v	x						
		v1	8	7x	5			
		x3		9	6		8x	
				x		v		v
			x2				7x	
	x		v			x	9	
			x9					
				x7				
x9								
	2		5	8			xv	v1